

Attorney Docket No. WIDC-021/00US
Express Mail No.

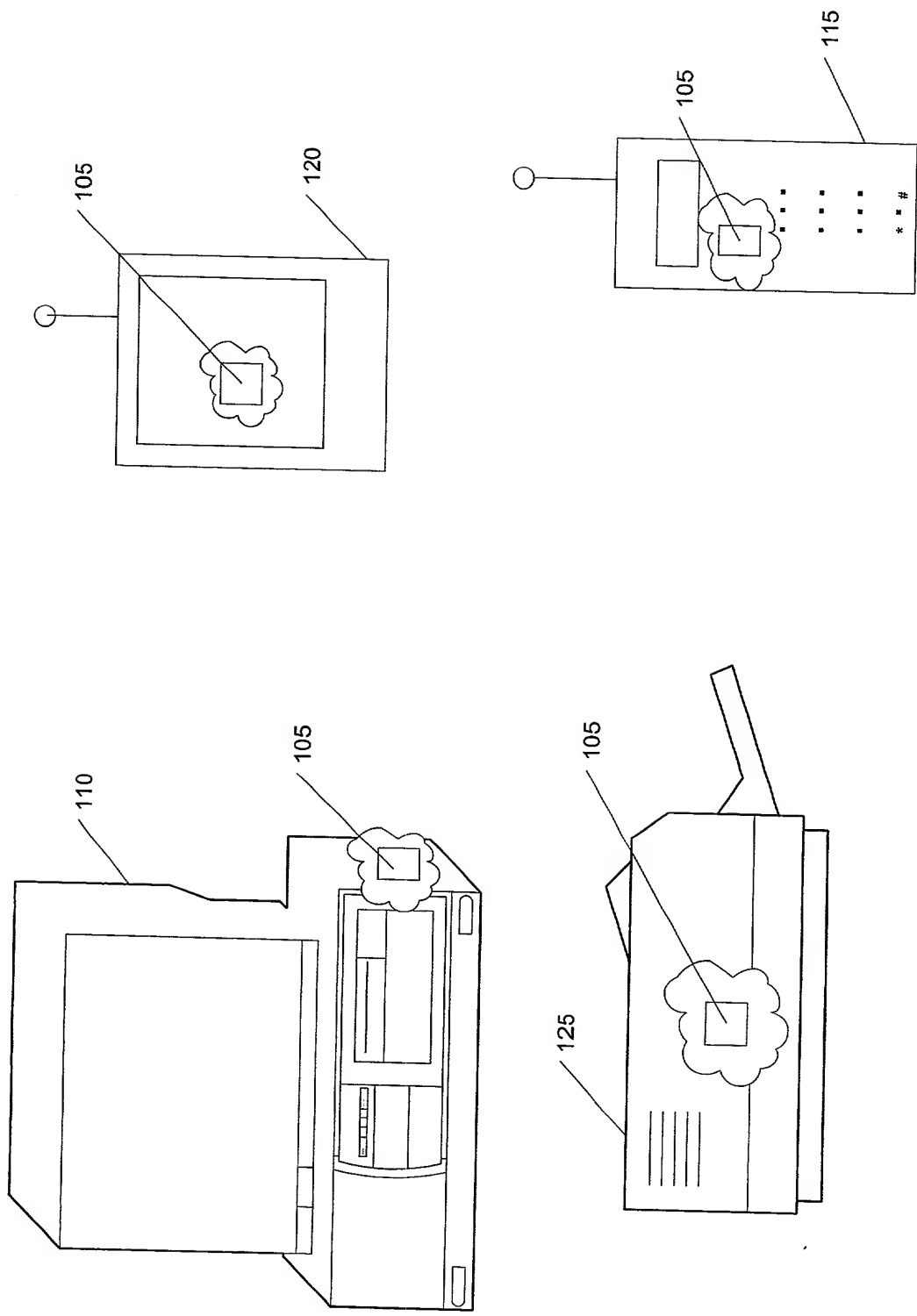
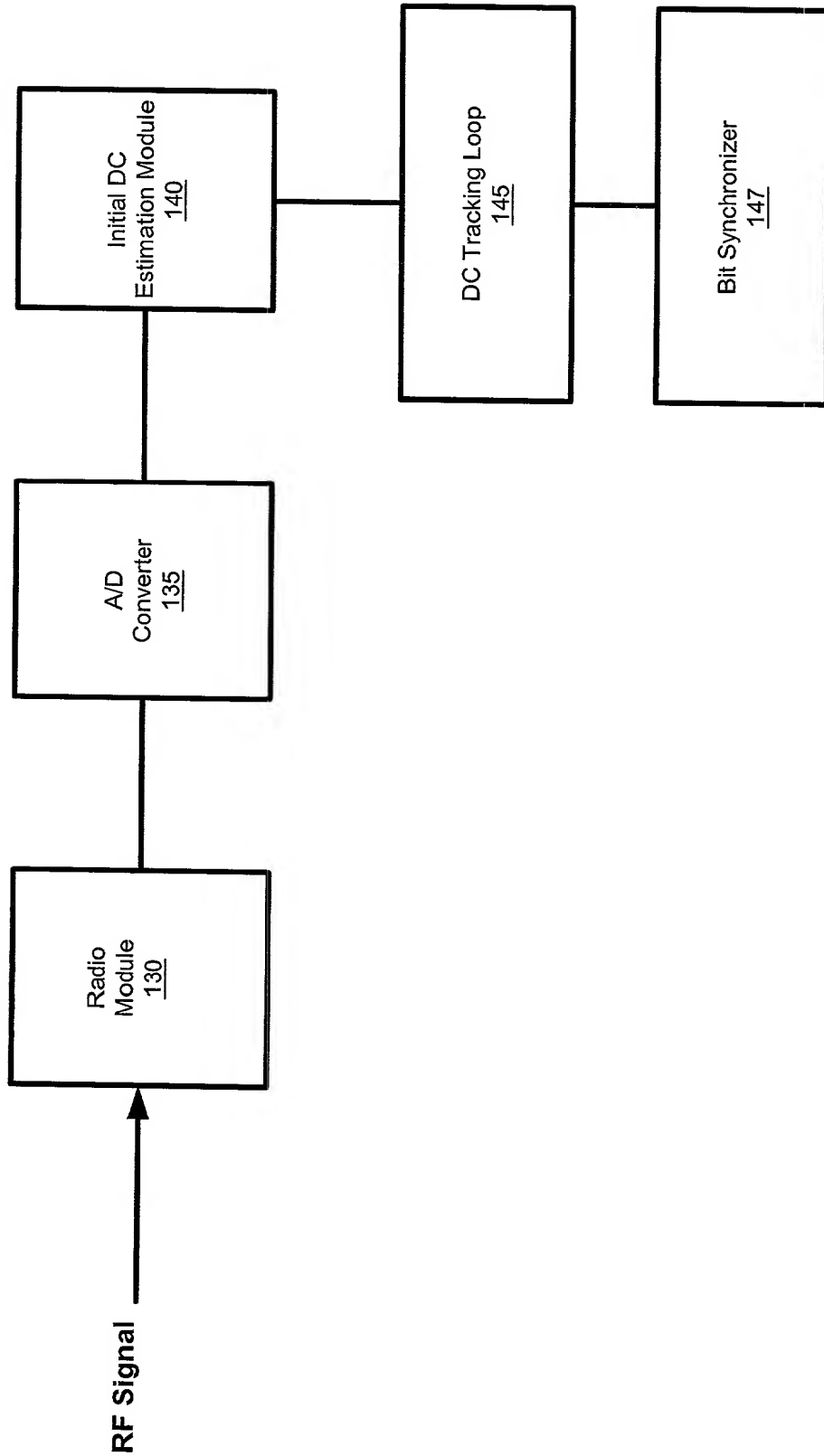


FIGURE 1.

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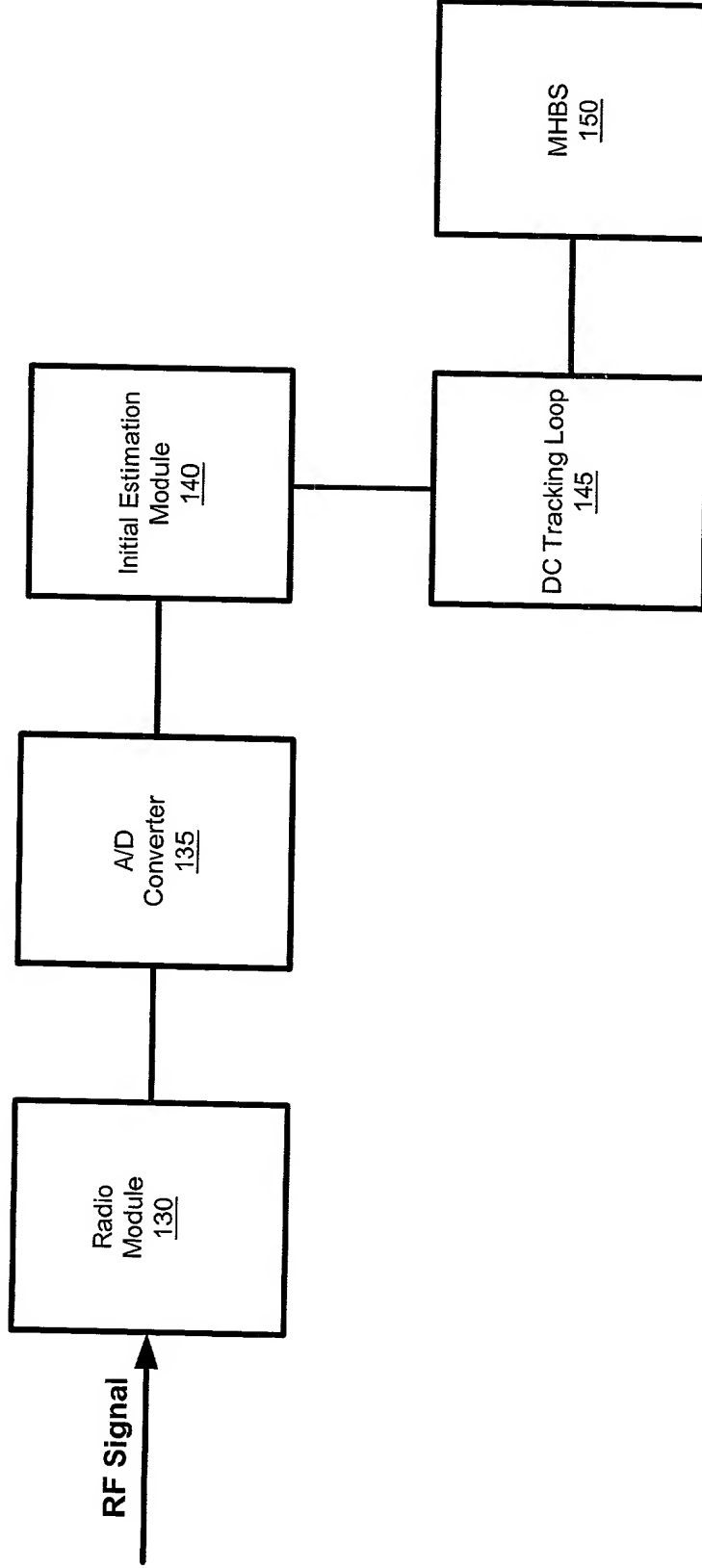


FIG. 5 is a block diagram of a digital filter for processing a signal from an A/D converter. The filter includes a feedback loop with a delay element and a summing junction. The input signal is processed by a bit up shift block and a bit down shift block. The output of the filter is a DC estimate offset.

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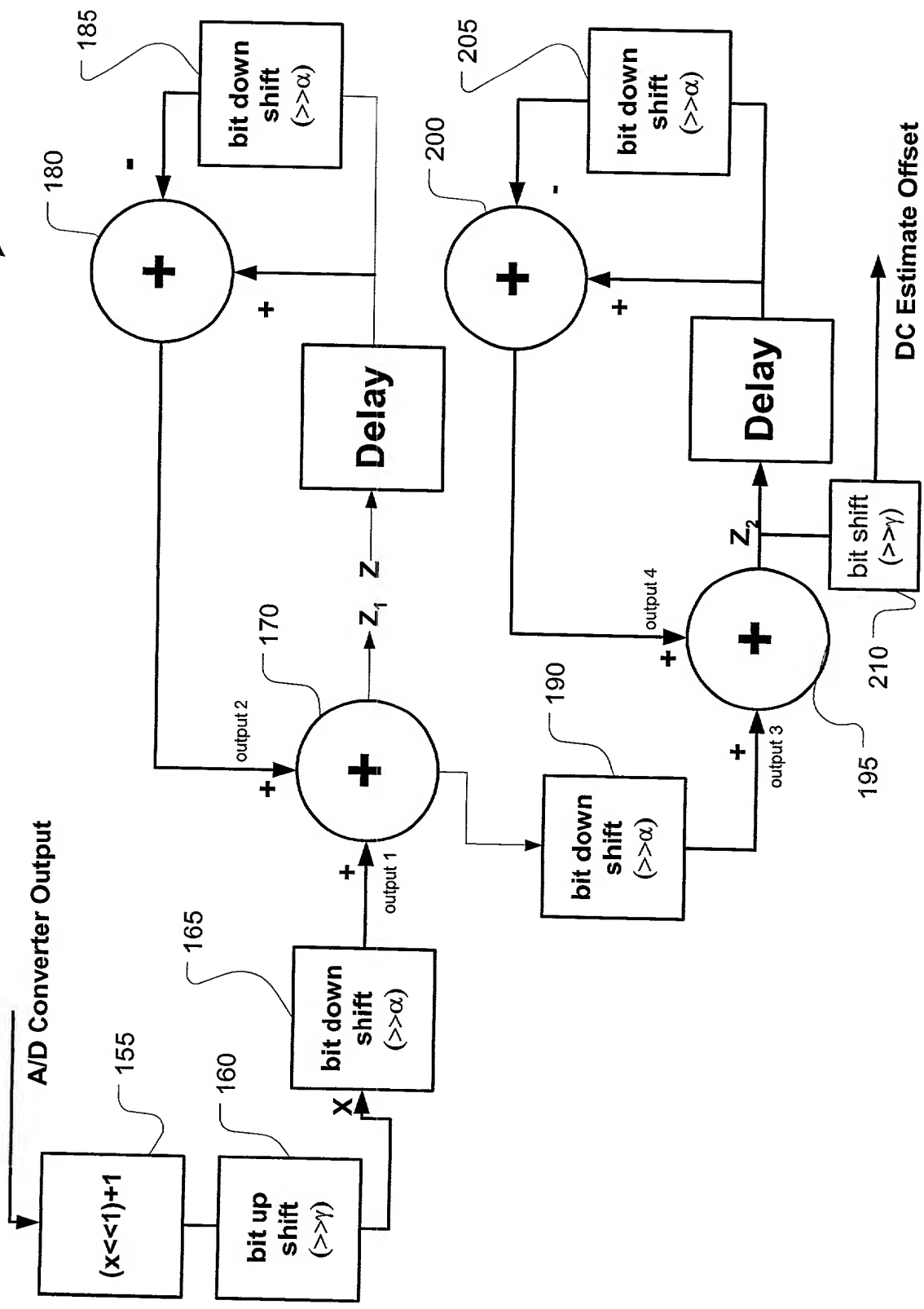
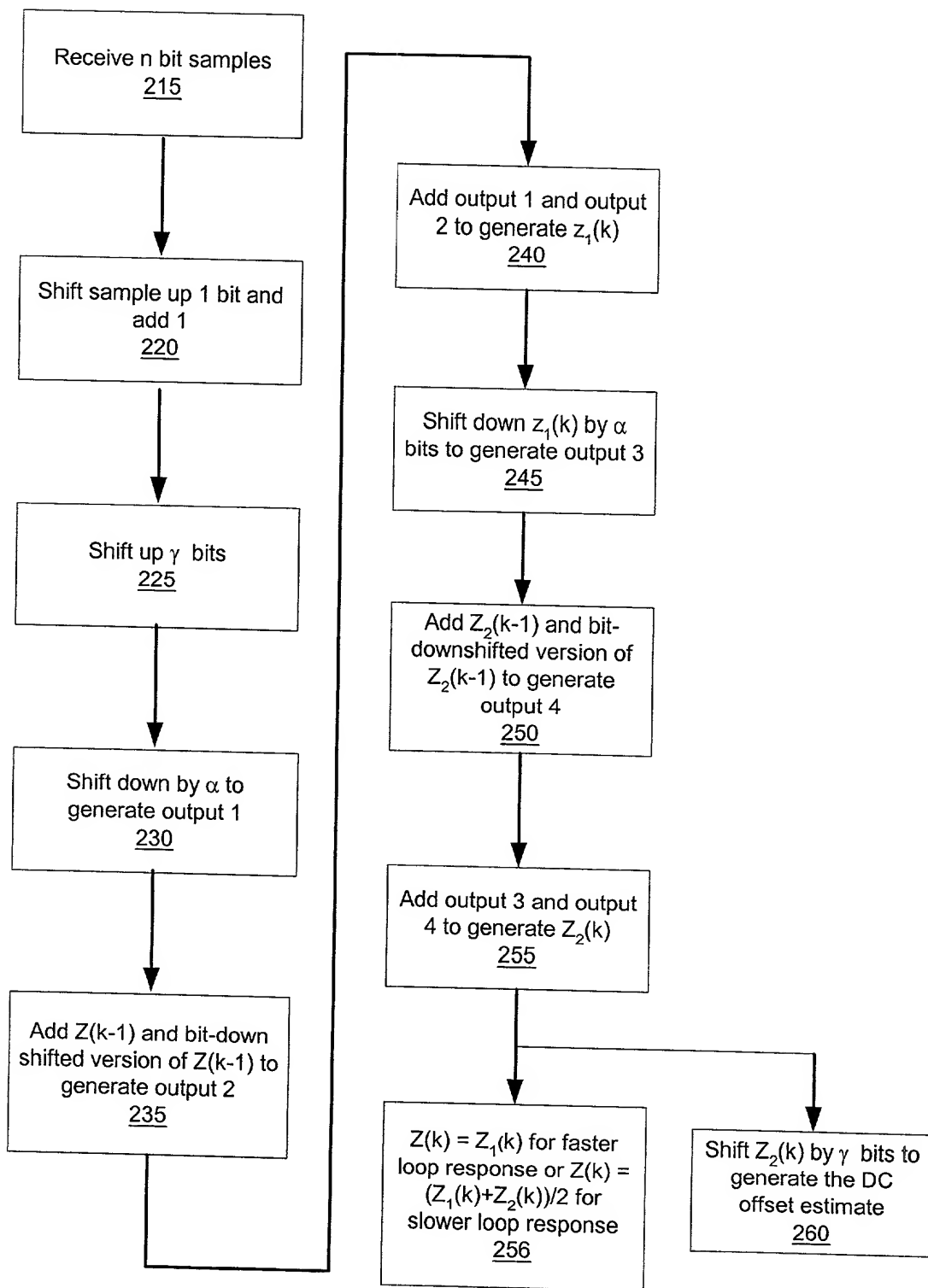
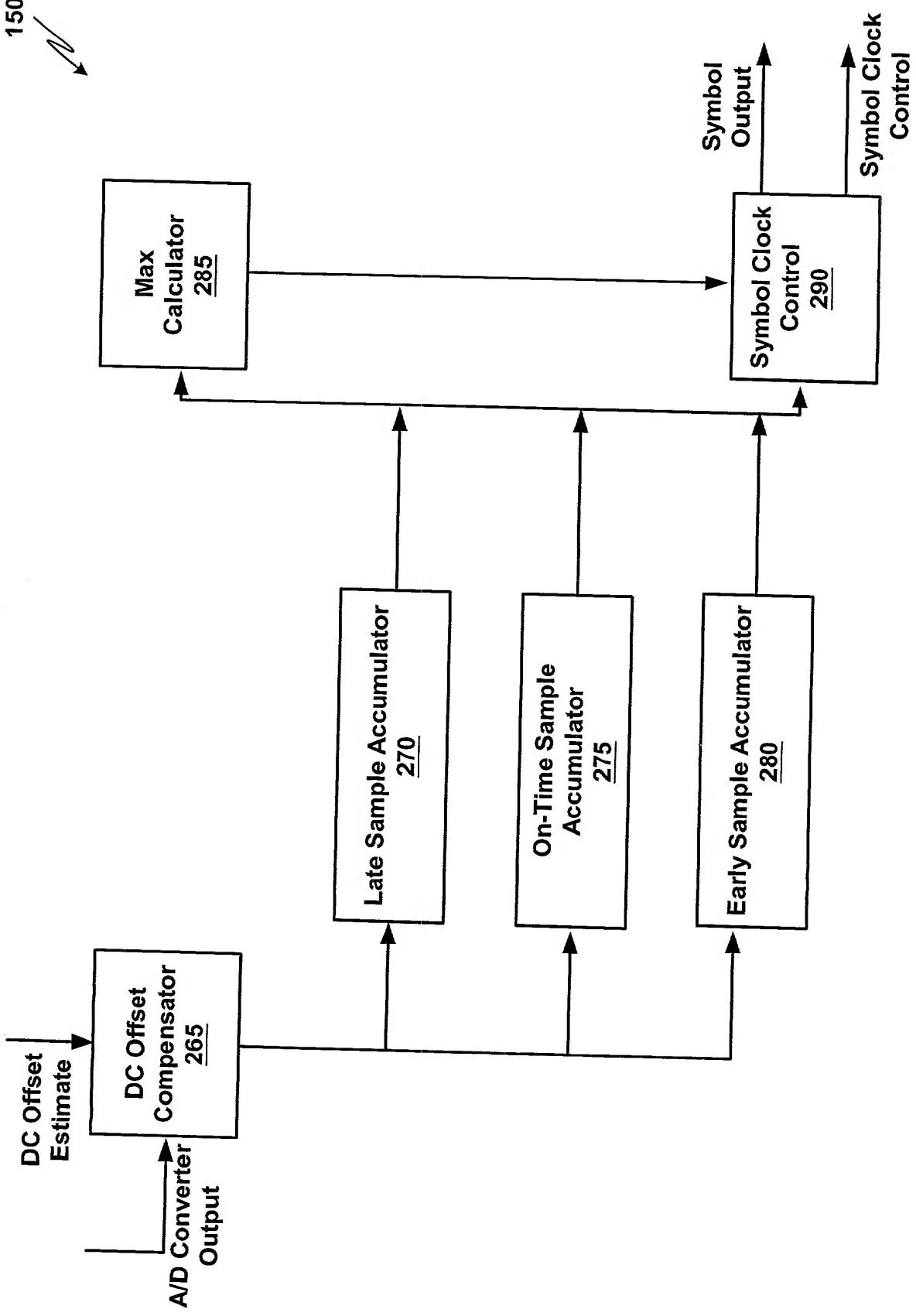


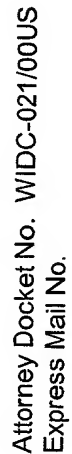
FIGURE 5.



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